



REVISION HISTORY

- REV A. Initial Design
- REV. B 5 VDC Power Supply circuit removed. 3.3 V to 5 V interface circuit added for Servo Signals.
- REV. C Connectors P2 and P3 moved 0.020 away from board edge on PCB design to allow panelized assembly.

Sheet: /		File: Quiver_Dual_Interface.sch	
Title:			
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